

Kai-Qi Huang

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Georgia Institute of Technology
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Education Georgia Institute of Technology | School of Electrical and Computer Engineering
Doctor of Philosophy in Electrical and Computer Engineering
Aug. 2017 – present
Master's degree expected Spring '21
GPA: 3.88

Shanghai Jiao Tong University | Department of Electronic Engineering
Bachelor of Science in Engineering in Electronic Science and Technology
Sept. 2013 – June 2017
GPA: 90.3/100 (Ranked 2/64)

Research Experience Hetero-Integration of Microwave and Millimeter Dielets
Centre for Co-design of Chip, Package, System (C3PS); Packaging Research Centre (PRC);
Georgia Institute of Technology
Supported by ASCENT, one of six centres in JUMP, a Semiconductor Research Corporation
(SRC) program sponsored by DARPA
Aug. 2018 – present

Antenna Integration and Miniaturization
Centre for Co-design of Chip, Package, System (C3PS); Packaging Research Centre (PRC);
Georgia Institute of Technology
Supported by ASCENT, one of six centres in JUMP, a Semiconductor Research Corporation
(SRC) program sponsored by DARPA
May 2018 – present

Design of Antenna and Feeding Network for 5G Higher-Frequency Band Applications
Bachelor's Thesis
Key Laboratory of Ministry of Education of Design and Electromagnetic Compatibility of
High-Speed Electronic Systems, Shanghai Jiao Tong University
Dec. 2016 – June 2017

Microwave Negative Group Delay Circuits Design
Key Laboratory of Ministry of Education of Design and Electromagnetic Compatibility of
High-Speed Electronic Systems, Shanghai Jiao Tong University
Aug. 2016 – Dec. 2016

Design of RF Analog Circuits for Communication and Instrumentation Applications
Texas Instruments – Shanghai Jiao Tong University Joint Laboratory of Analog Technology
Oct. 2013 – June 2017

Publication	Packaging Approaches for mmWave and Sub-THz Communication S. Ravichandran, K.-Q. Huang, Mutee ur Rehman, S. Erdogan, A. Watanabe, N. Nedumthakady, F. Liu, M. Kathaperumal and M. Swaminathan 2019 IEEE MTT-S International Microwave Conference on Hardware and Systems for 5G and Beyond (IMC-5G)	
	A Feeding Structure of Patch Antenna with Higher-Order Mode Suppression K.-Q. Huang, L.-S. Wu, X. Zhang, J.-F. Mao 2017 National Conference on Antennas, Xi'an, China	Oct. 2017

Awards	Intel – GT Outstanding RFIC Student Designer Award Sponsored by Intel Corporation	Dec. 2018
	Outstanding College Graduate of the City of Shanghai Awarded by the Ministry of Education of Shanghai	June 2017
	Academic Excellence Scholarship (Second Class) Awarded by Shanghai Jiao Tong University	Dec. 2016
	National Scholarship Awarded by the Ministry of Education of China	Nov. 2016
	Academic Excellence Scholarship (First Class) Awarded by Shanghai Jiao Tong University	Dec. 2015
	National Scholarship Awarded by the Ministry of Education of China	Nov. 2015
	National Undergraduate Electronic Design Contest (NUEDC) TI Cup of Shanghai, Third Prize Awarded by the Ministry of Education of Shanghai and the NUEDC Committee	Nov. 2015
	Academic Excellence Scholarship (Third Class) Awarded by Shanghai Jiao Tong University	Dec. 2014

Extracurricular Experience	Graduate Research Assistant Fall 2018 – present
	Teaching Assistant for the Basic Communication Circuits Lab. Course Fall 2016
	Academic Internship at the Central Laboratory of Department of Electronic Engineering, Shanghai Jiao Tong University Summer 2015
	Chief Manager of the Texas Instruments – Shanghai Jiao Tong University Joint Laboratory of Analog Technology Fall 2014 – Spring 2017
	Volunteer at the University Library of Shanghai Jiao Tong University Spring 2014
	Member of Student Science and Technology Association of Shanghai Jiao Tong University Fall 2013 – Spring 2015